

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	27986	"NEC CORPORATION"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 17:34
L2	1	L1 and (((("frequency" adj ("multiplication" or "division") adj circuit\$1) with "ratio") with (("n" or "N") with ("positive" adj "integer"))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 17:35
L3	2	(((("frequency" adj ("multiplication" or "division") adj circuit\$1) with "ratio") with (("n" or "N") with ("positive" adj "integer"))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 17:37
L4	70	((("clock" with "control" adj ("system" or "device" or "unit" or "apparatus" or "circuit" or "method")) with ("frequency" adj ("multiplication" or "division") adj circuit\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 17:36
L5	2	I4 and (((("frequency" adj ("multiplication" or "division") adj circuit\$1) with "ratio") with (("n" or "N") with ("positive" adj "integer"))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 17:37
L6	70	I4 and (("clock" with control\$3) with ("frequency" with circuit\$1))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 17:37
L7	0	(((("low" with "power") adj "consumption" adj "mode") with (chang\$3 with ("frequency" adj "multiplication" adj "ratio"))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:08
L8	114	"5778237"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:09
L9	1704	713/322	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:08
L10	0	I9 and (((("low" with "power") adj "consumption" adj "mode") with (chang\$3 with ("frequency" adj "multiplication" adj "ratio"))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:10
L11	4992	713/300	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:09

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L12	0	l11 and (((("low" with "power") adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio"))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:10
L13	2782	713/320	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:10
L14	0	l13 and (((("low" with "power") adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio"))))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:11
L15	5	l13 and (("power"adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:11
L16	5	l11 and (("power"adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:22
L17	817	713/330	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:22
L18	1	l17 and (("power"adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:23
L19	1483	713/340	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:23
L20	1	l19 and (("power"adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:26
L21	1926	713/600	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:26
L22	1	l21 and (("power"adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:26
L23	20	"KURAKANE, HIROSHI"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:40

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L24	20	L23 and "KURAKANE, HIROSHI"	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 18:40
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2232	713/500	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 21:54
L2	1	I1 and (("power"adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 21:55
L3	1338	713/501	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 21:55
L4	1	I3 and (("power"adj "consumption" adj "mode") with (chang\$3 with ("frequency" ajd "multiplication" adj "ratio")))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2007/10/10 21:55

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frequency multiplication ratio with 1/N

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Patents

Patents 1 - 10 on **frequency multiplication ratio with 1/N**. (0.29 seconds)

Frequency multiplier using a voltage controlled delay circuit

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US Pat. 5955902 - Filed Mar 5, 1997 - Kabushiki Kaisha Toshiba
 An N multiplying signal, being composed from a signal whose duty **ratio** is not%, produces **frequency** jitters. Under these situation, in order to make the ...

Frequency multiplying circuit having a greater multiplying ratio

US Pat. 6005420 - Filed Mar 31, 1997 - Kabushiki Kaisha Toshiba
 In order to secure the multiplying **ratio** M of the subsequent stage **frequency** multiplier, the delay amount per stage of the delay circuit of the subsequent ...

Frequency spectrum analyzer with high C/N ratio

US Pat. 6166533 - Filed Oct 13, 1998 - Advantest Corp
 The fourth local signal to the **frequency** mixer 14 has a **frequency** which is 1/N of the first local signal oscillator 31. The **frequency** mixer 14 provides a ...

SINGLE SIDEBAND MODULATOR

US Pat. 3349184 - Filed May 17, 1965
 This invention is not limited to a compression **ratio**, of 5, which is merely ...
 first complex variable **frequency** information-bearing signal by a **ratio** 1/n, ...

Bandpass filter circuit

US Pat. 4310803 - Filed Aug 20, 1979
 40 a **frequency** divider operatively connected to the output of said second bandpass filter and having a **frequency** dividing **ratio** 1/N; a multiplier means ...

Pulse frequency multiplier circuit

US Pat. 4353030 - Filed Sep 5, 1980 - Hitachi, Ltd.
 ... shown in FIGS' wave which reaches a reference voltage for a 1/n per- __, ...
 output pulses from the comparator circuit 3 and the have the **ratio** of 1:n. ...

Phase locked loop (PLL) circuit

US Pat. 6163184 - Filed Dec 9, 1998 - Lucent Technologies, Inc.
 6, the command signal applied to the divider network 16 determines the **ratio** N.
 The **ratio** N determines the value of f_0/N . Also, outputs of the divider ...

Power supply control device

US Pat. 5995390 - Filed Feb 1, 1999 - Toko, Inc.
 Further, high- **frequency** and low-**frequency** pulse output signals, whose pulse frequencies are at a **ratio** of 1 to n, are obtained from the master and slave ...

Phase shift keying signal demodulation method and device

US Pat. 5912930 - Filed Apr 1, 1997 - NEC Corporation

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set CPU to a low power consumption mode by

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Patents Patents 1 - 2 on **set CPU to a low power consumption mode by changing a frequency multiplication ratio**. (0.22)

Data processor and single-chip microcomputer with [Sort by relevance](#) | [Sort by date \(new first\)](#) | [Sort by date \(old first\)](#)
changing clock frequency ...

US Pat. 5778237 - Filed Dec 14, 1995 - Hitachi, Ltd.

19. the 20 **frequency** ratios of the clock signals are **set** at the **CPU** clock ...
and the **low power consumption** is preferred to the performance of the central ...

Information processing apparatus with clock generating circuit and ...

US Pat. 6937082 - Filed Aug 25, 2003 - Renesas Technology Corp.

Thus, the processing performance is improved, and **power consumption** ... as or is
a multiple **ratio** of a **frequency** of a reference clock signal by **changing** an ...

set CPU to a low power consumption mode b

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